Optics & Photonics

Budowa Silesia Photonics

ACAP with optical module

Overview

Heterogeneous computing card combining hard ARM processor cores, large FPGA fabric, AI Engines, and high-bandwidth interfaces Designed specifically for sensor interface, AI workloads, digital signal processing, video processing, application co-processing, and secure networking HPEC focus. Heterogeneous computing card combining hard ARM processor cores, large FPGA fabric, AI Engines, and high-bandwidth interfaces Designed specifically for sensor interface, AI workloads, digital signal processing, video processing, application co-processing, and secure networking HPEC focus. The SCFE6931 leverages Versal ™ ACAP (Adaptive Compute Acceleration Platform) technology from AMD® to create an FPGA processing board that operates up to 20× faster than former FPGA implementations. The dual AI core, fiber-optic interfaces and network-on-chip (NoC) come together to redefine how. The XPedite2770 is a high-performance, reconfigurable, conduction-cooled, 3U VPX processing module based on the AMD (formerly Xilinx) Versal® Prime Adaptive Compute Acceleration Platform (ACAP). The V6063 provides options for Versal® Prime or Versal® AI Core part selection.

Article Content

AMD Allies with Ranovus on Data Center Photonics ...

The co-packaged optical (CPO) demonstration system built by Xilinx and Ranovus. It incorporates the former''s Versal ACAP with the latter''s Odin Analog-Drive CPO 2.0.

V1163-12-Port-Rugged-XMC-ACAP-Card-Datasheet

Radar, SIGINT, video, storage, medical imaging, and embedded communications systems all have the ability to benefit from the V1163 module. The V1163 provides electrical and optical IO options ...

Ranovus® demonstrates industry''s lowest power consumption ...

RANOVUS'' Odin® is a low latency, high density, protocol agnostic, and standards-based optical engine that delivers massive optical interconnect bandwidth with industry-leading cost and ...

XPedite2770 | 3U VPX Versal® Prime ACAP-Based Module

It integrates SecureCOTS™ technology with a Versal® Prime VM1402 ACAP for hosting custom functions to protect data from being modified or observed and provides an ideal solution when ...

V6063-3U-VPX-Versal-ACAP-ASOC-FPGA-Module-Datasheet

The V6063 provides twelve (12) full duplex optical ports supporting from 1-25Gb/s per lane, FPGA fabric resources, ARM processor cores, and AI/ML hard cores. The V6063 can also be used adjacent to ...

ASoC FPGA Optical I/O Module

The V6063 serves as a standalone data interface and processing solution in a single 3U VPX module. The V6063 provides twelve (12) full duplex optical ports supporting from 1-25Gb/s per lane, FPGA ...

Two New SOSA Aligned 3U VPX ACAP (FPGA) Modules | New

New Wave Design is proud to announce the addition of SOSA aligned 3U VPX solutions — the new V6061 and V6063 VPX modules — to its product line.

ACAP Optical I/O Module

The V6063 is a next generation heterogeneous embedded computing 3U VPX module featuring the Xilinx® Versal® Adaptive Compute Acceleration Platform (ACAP), rugged optical and electrical high ...

Optical module design resources | TI

Design requirements Modern optical module designs often require: Reduced power consumption to control and limit module temperature rise. Dynamic and precise control of laser diodes to regulate ...

SCFE6931 ACAP Processing Module | Mercury Systems

The dual AI core, fiber-optic interfaces and network-on-chip (NoC) come together to redefine how much capability can be fit in a 6U module. The SCFE6931 can process and move data faster than ever ...

Contact Us

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